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LT6911B --- Product Brief

Features

z HDMI1.4 Receiver
- ƒ Compliant with the HDMI 1.4 specification with TMDS data rates up to 3.4Gbps per channel
- ƒ Support HDCP 1.4
- ƒ Adaptive receiver Equalization for PCB, cable and connector losses

z Single/Dual-Port/Quad-Port MIPI® DSI/CSI Transmitter
- ƒ Compliant with DCS1.02, D-PHY1.2& DSI1.02 &CSI-2 1.0
- ƒ 1 Clock Lane, and 1~4 Configurable Data Lanes per port
- ƒ 1/2/4 configurable port
- ƒ 80Mb/s~1.5Gb/s per data lane
- ƒ Data lane and polarity swapping
- ƒ Maximum 64pixels overlap for each half
- ƒ Both non-burst and burst video mode supported
- ƒ Support RGB666, Loosely RGB666, RGB888, RGB565, 16-bit YCbCr4:2:2,20-bit YCbCr4:2:2,24-bit YCbCr 4:2:2, 12-bit YCbCr4:2:0Video Format
- ƒ Video stream copy mode for each single/dual-port
- ƒ Side-by-side 3D support
- ƒ Port swap

z Miscellaneous
- ƒ 3.3V/1.2V Supply Power
- ƒ Internal CSC support conversions between YCbCr 4:4:4 and RGB, and between YCbCr 4:2:2 and YCbCr 4:4:4
- ƒ Support SPDIF and 8-channel IIS audio output
- ƒ Support 100KHz and 400KHz I2C slave
- ƒ Power from phone or adapter mode selection
- ƒ Integrated Microprocessor
- ƒ Embedded EDID shadow.
- ƒ Temperature Range: -40°C ~ +85°C
- ƒ ESD 4kV HBM

Description

The LT6911B is a high performance HDMI1.4 to MIPI®DSI/CSI chip for VR/Smart phone/Display application.

For MIPI®DSI/CSI output, LT6911B features configurable single-port or dual-port or quad-port MIPI®DSI/CSI with 1 high-speed clock lane and 1~4 high-speed data lanes operating at maximum 1.5Gb/s/lane, which can support a totalbandwidth of up to 24Gbps. LT6911B supports Burst mode DSI vhd1S T4( )]TJ 0 -