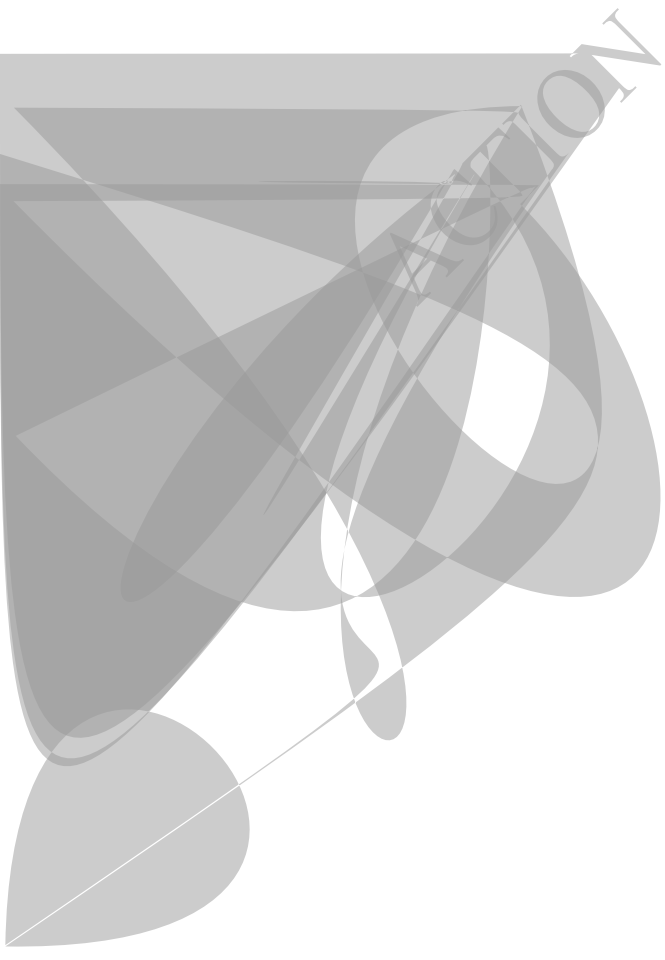
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2 Feature

The AM8352 provides high level of system integration to support a wide variety of applications. The features of the AM8352 include:

✓ **32BIT RISC CORE**
- 32K byte instruction cache and data cache
- F/W can program from DC up to 800MHz transparently
- DSP instruction for multi-media acceleration
- Static design allows changing clock at run-time for power saving

✓ **VIDEO ENCODER**
- Support ISO/IEC 14496-10/YU-T Rec.H.264
- Base Profile,Level 1-5.1

  Main Profile ,levels 1-5.1

  High Profile ,levels 1-5.1
- Support JPEG ITU-T Rec.T81(09/92),Baseline interleaved/JFIF
- 60 frames per second at 1920x1080 resolution for video all format

✓ **IMAGE/VIDEO COMPRESSOR**
- Support upto 1080P Resolution
- Support 8bit YCbCr 4:2:2 & YCbYCr/RGB
- Adaptive compress ratio up to 1:6

✓ **OSD**
- 1,2 OSD bitmap data width
- 256x128 size in 2 bit or 256x256 in 1 bit

✓ **DISPLAY INTERFACE**
- HDMI Rx support, industry standard compliance HDMI 1.3a

✓ **AUDIO**
- I2S IN & I2S OUT interface
- Support 32 levels volume control

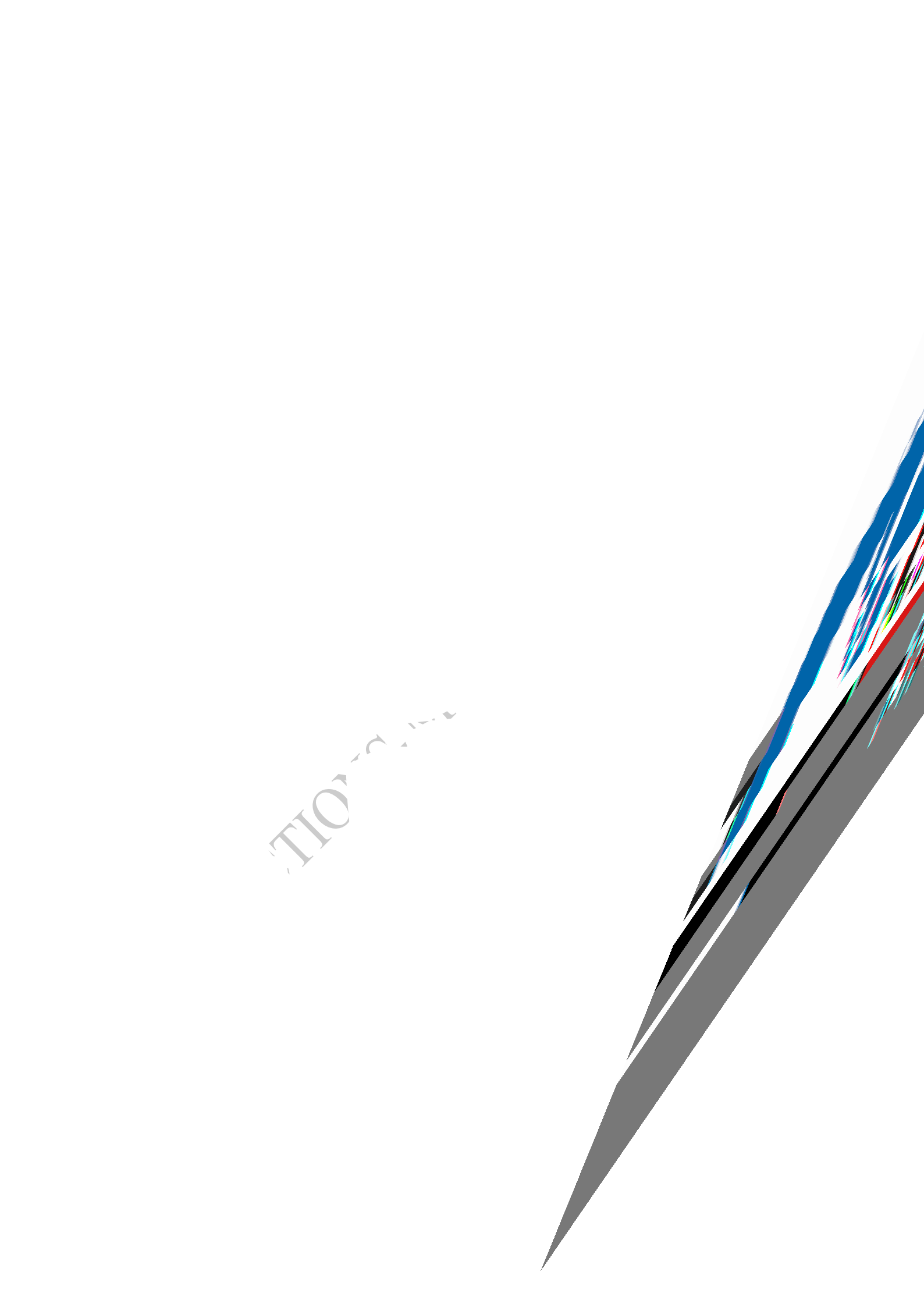
✓ **MEMORY Storage**
- DDR2/3 SDRAM up to 4Gb @ 16bit up to 1066Mbps
- OTP ROM 64bit Chip ID

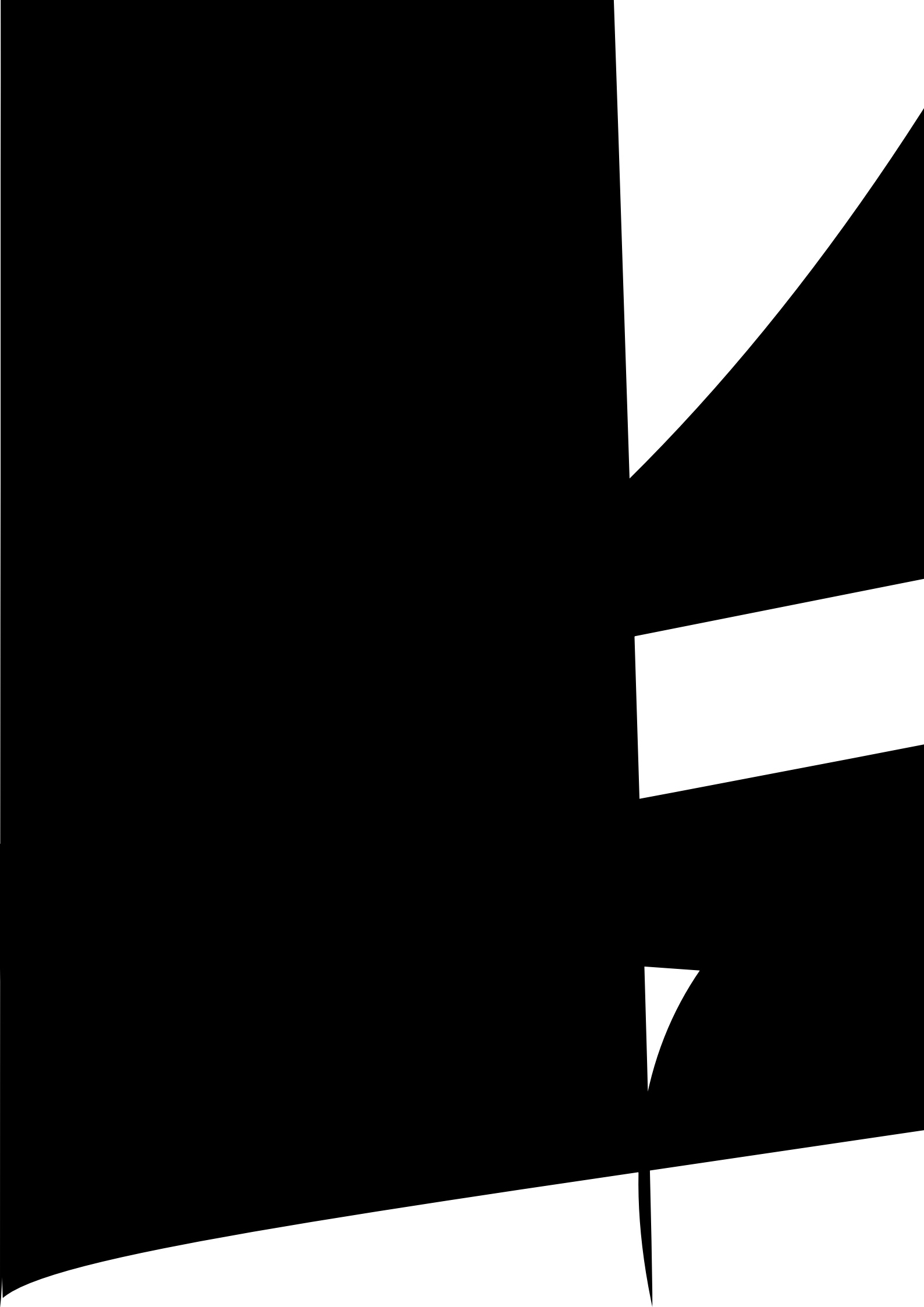
✓ **DMA CONTROLLER**
- 8 physical channels and 4 bus channels

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